

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: STANLEY D. STEARNS ET AL

Serial No.:

Group No.:

Filed:

Examiner:

For: OPTICALLY GENERATED ISOLATED FEEDBACK STABILIZED BIAS

MAIL STOP: Patent Application  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

List of Sections Forming Part of This Information Disclosure Statement

The following sections are being submitted for this Information Disclosure Statement:

1. Preliminary Statements
2. Forms PTO 1449 (now PTO/SB/08A and 08B)
3. Copies of Listed Information Items Accompanying This Statement
4. Concise Explanation of English Language Listed Information Items
5. Identification of Person(s) Making This Information Disclosure Statement

**Section 1. Preliminary Statements**

Applicant(s) submits herewith patents, publications or other information, of which they are aware that they believe may be material to the examination of this application, and in respect of which, there may be a duty to disclose.

---

**CERTIFICATION UNDER 37 C.F.R. § 1.10**

I hereby certify that this Information Disclosure Statement and the documents referred to as attached thereto are being deposited with the United States Postal Service on the date listed below, in an envelope as "Express Mail Post Office to Addressee," mailing Label Number ER216163965US, addressed to the: MAIL STOP: Patent Application, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450

7/15/03  
Date

  
Kenneth A. Keeling  
Registration No. 31,842

The filing of this information disclosure statement shall not be construed as a representation that a search has been made (37 CFR 1.97(g)), an admission that the information cited is, or is considered to be, material to patentability, or that no other material information exists.

The filing of this information disclosure statement shall not be construed as an admission against interest in any manner. Notice of January 9, 1992, 1135 O.G. 13-25, at 25.

## **Section 2. PTO 1449 (Now Forms PTO/SB/08A and 08B)**

PTO Form 1449 is attached hereto.

## **Section 3. Copies of Listed Information Items Accompanying This Statement**

Legible copies of all items listed in Form PTO-1449 accompany this information statement.

## **Section 4. Concise Explanation of English Language Listed Information Items**

1. U.S. Patent No. 5,541,519 issued to Stearns et al.  
Discloses a pulsed rare gas photoionization detector apparatus incorporating a closed chamber for receiving a carrier gas flowing there through between inlets and outlets.
2. U.S. Patent No. 5,594,346 issued to Stearns et al.  
Discloses a pulsed discharge photoionization detector comprising a plurality of closed chambers for receiving different types of carrier gas flowing there through between inlets and outlets.
3. U.S. Patent No. 5,767,683 issued to Stearns et al  
Discloses a detection system that measures very small concentrations of compounds of interest within gaseous samples.
4. U.S. Patent No. 4,375,596 issued to Hoshi  
Discloses a reference voltage generator circuit, which overcomes variations in a power supply by dividing the power supply voltage to create two output signals, uniformly modifying the signals in opposite polarity, then averaging the resulting signals to generate a constant value of reference voltage.
5. U.S. Patent No. 4,380,706 issued to Wrathall  
Discloses a temperature stable voltage reference source, which uses a differential amplifier with an output coupled to an additional amplifying stage, involving two bipolar

transistors, wherein the emitter of one transistor is larger than the emitter of the other transistor. Cascaded emitter followers are used between the two amplifying stages to develop a higher voltage, which is fed back into the inputs of the differential amplifier, thereby establishing a more independently stable reference voltage circuit.

6. U.S. Patent No. 4,471,290 issued to Yamaguchi  
Discloses a substrate bias generating circuit responsive to the output signal of the oscillator circuit, which includes a voltage divider connected between the output terminal of the bias generating circuit and a ground terminal, and a level sensor for producing a control signal to the oscillator circuit when it is detected that the output voltage of the voltage divider reaches a predetermined value, to thereby stop the oscillating operation of the oscillator circuit
7. U.S. Patent No. 5,262,989 issued to Lee et al.  
Discloses a circuit for sensing back-bias levels in a semiconductor device that causes the voltage pump circuit to adjust output to reach and maintain a desired voltage level.
8. U.S. Patent No. 3,975,649 issued to Kawagoe et al.  
Discloses a temperature compensation circuit that uses a high value resistor and at least one field-effect transistor for connection between a circuit to be compensated and the power source, such that the when ambient temperature of the circuit increases the current flowing through the field-effect transistor decreases.
9. U.S. Patent No. 4,794,247 issued to Stineman, Jr..  
Discloses using an integrating amplifier with a feedback capacitor, to stabilize the bias signal from a photovoltaic detector, while reducing the noise effect.
10. U.S. Patent No. 4,843,265 issued to Jiang  
Discloses a temperature compensating circuit that generates inverse variations in a field-effect transistor, achieved by charging a capacitor to a voltage and discharging the capacitor through a field-effect transistor in response to the fluctuations.
11. U.S. Patent No. 5,805,062 issued to Pearlman  
Discloses an isolation amplifier that transmits data to a receiver via a current loop, where the isolated portion of the circuit is powered by a photovoltaic array illuminated by a light source, optionally an array of same frequency light-emitting diodes.

## **Section 5. Identification of Person(s) Making This Information Disclosure Statement**

The person making this statement is:

- (a)  the inventor(s) who signs below.
- (b)  an individual associated with the filing and prosecution of this application.

(c) ■ the practitioner who signs below on the basis of the information:

- supplied by the inventor(s).
- supplied by an individual associated with the filing and prosecution of this application (37 CFR 1.56(c))
- in the practitioner's file.

Respectfully submitted,



Kenneth A. Keeling  
Registration No. 31,842  
James E. Hudson III  
Registration No. 41,081

July 15, 2003  
Keeling Hudson L.L.C.  
901 North Post Oak Road  
Houston, Texas 77024  
713-680-1447  
713-680-0726 (Fax)

Please type a plus sign (+) inside this box →

PTO/SB/08A (08-00)

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

<p>Substitute for form 1449A/PTO</p> <p><b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b></p> <p><i>(use as many sheets as necessary)</i></p>				<p><b>C mplete if Known</b></p>	
<p>Application Number</p>					
<p>Filing Date</p>					
<p>First Named Inventor</p>				<p><b>STANLEY D. STEARNS ET AL</b></p>	
<p>Group Art Unit</p>					
<p>Examiner Name</p>					
<p>Sheet</p>		<p>1</p>		<p>of</p>	
<p>1</p>				<p>Attorney Docket Number</p>	

## U.S. PATENT DOCUMENTS

## FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
-----------------------	--	--------------------	--

**\*EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Unique citation designation number. <sup>2</sup> See attached Kinds of U.S. Patent Documents. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

**Burden Hour Statement:** This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U. S. Patent and Trademark Office, Washington, DC 20231. **DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO:** Assistant Commissioner for Patents, Washington, DC 20231.